

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of: )  
Shunpei YAMAZAKI et al. ) Art Unit: Unassigned  
Based on Foreign Application )  
Serial Nos. JP 2000-403098 and 2000-384543 ) Examiner: Unassigned  
Filed: December 19, 2001 )  
For: METHOD OF MANUFACTURING SEMICONDUCTOR)  
DEVICE AND SEMICONDUCTOR DEVICE )



**INFORMATION DISCLOSURE STATEMENT**


Commissioner for Patents and Trademarks  
Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the reference(s) listed on the attached Form PTO-1449 be made of record in the above-identified application. Copies of the references are submitted herewith in accordance with 37 C.F.R. 1.98(a).

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380.

Respectfully submitted,

  
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Form PTO-1449  
(Rev. 8-83)U.S. Department of Commerce  
Patent and Trademark Office

Atty Docket 0756-2410

Serial No.

## INFORMATION DISCLOSURE STATEMENT

Applicants: Shunpei YAMAZAKI et al.

Filing Date: December 19, 2001

Group Art Unit:

## U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date (if appropriate)
	2001/0034088 A1	10/25/2001	Nakamura et al.			
	3,535,775	10/27/1970	Garfinkel et al.			
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U.S. PTO  
10/020961  
12/19/01

Examiner

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\*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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	6,160,268	12/12/2000	Yamazaki			
	6,165,824	12/26/2000	Takano et al.			

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Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
	08-078329	03/22/1996	JP			Eng Abst	

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